

First Named Inventor	Christophe Chevallier	<u>COMMUNICATION</u> <u>REGARDING DIVISIONAL</u> <u>APPLICATION</u>
Serial No.	Unknown	
Filing Date	Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.184US03	
Title: CHECKING LAYOUT ACCURACY IN INTEGRATED CIRCUIT DESIGNS		

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Prior to taking the above-entitled patent application under examination, please review the following comments:

COMMENTS

This is a divisional application of application Serial No. 10/199,727 (the '727 application), titled LINE WIDTH CHECK IN LAYOUT DATABASE, filed July 19, 2002 (pending). The '727 application was subjected to a Restriction Requirement by Examiner Brandon Bowers in the Office Action mailed August 26, 2003. Non-elected claims 6 and 7 of the '727 application respectively correspond to claims 1 and 2 of the present divisional application. Applicant contends that the remaining claims of the present divisional application comport with the subject matter of non-elected claims 6 and 7 of the '727 application.

The title has been amended to conform the title to the claims. The specification has been amended to add a cross reference to the '727 application and to conform the Summary to the claims. The Abstract has been amended to conform it to the claims. Formal drawings are submitted.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at 612 312-2208.

Respectfully submitted,

Date: 02-19-2004

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